APPROVED			
BY	CLASS	SUBCLASS	
DRAFTSMAN			

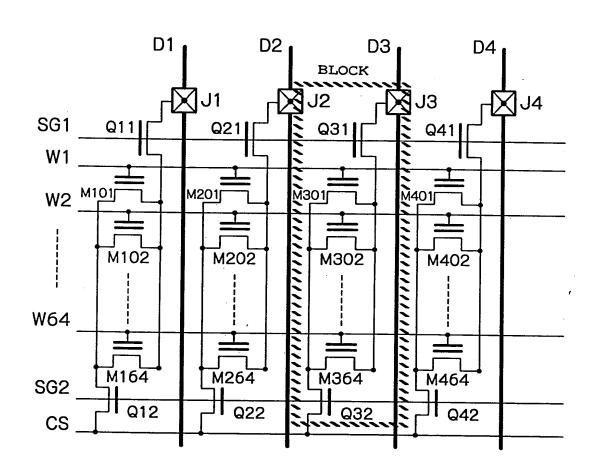


FIG. 1 (PRIOR ART)

	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		!

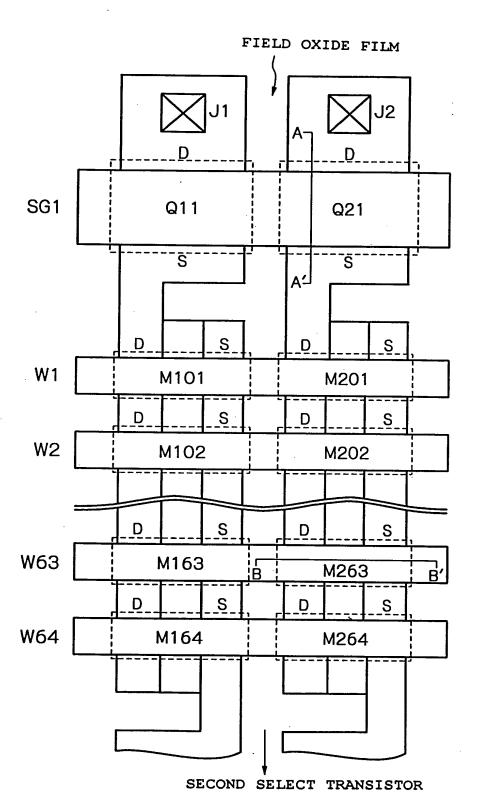


FIG. 2 (PRIOR ART)

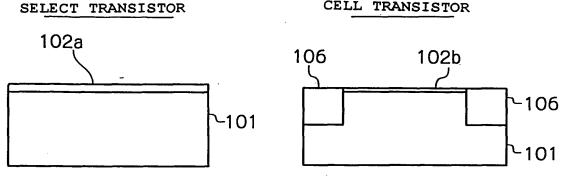
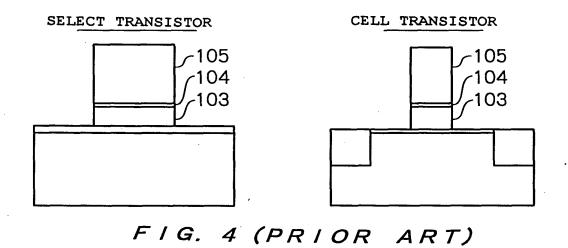


FIG. 3 (PRIOR ART)



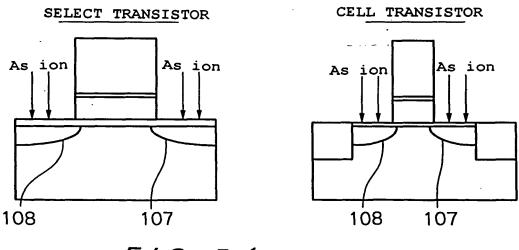
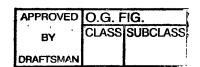
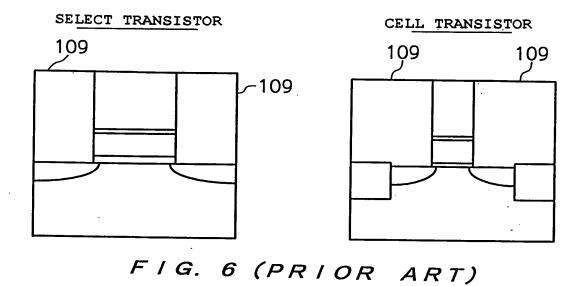
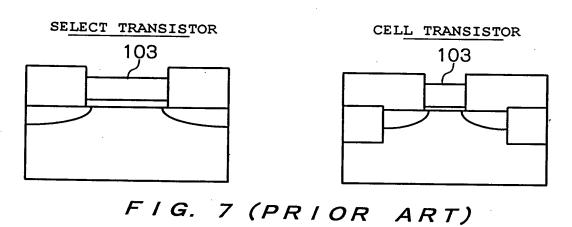


FIG. 5 (PRIOR ART)







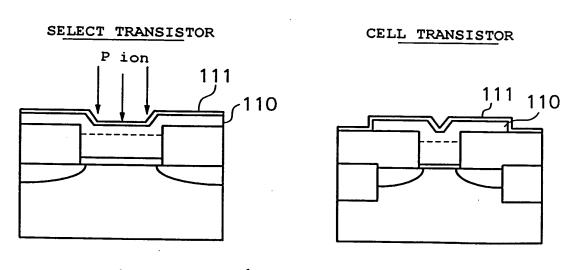
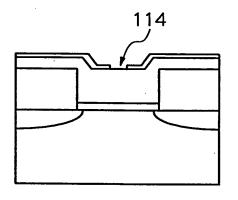


FIG. 8 (PRIOR ART)

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		ĺ



CELL TRANSISTOR



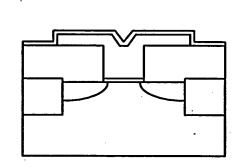
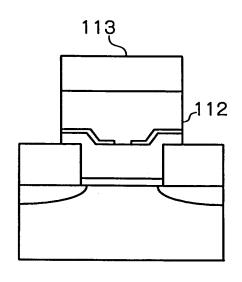


FIG. 9 (PRIOR ART)

SELECT TRANSISTOR

CELL TRANSISTOR



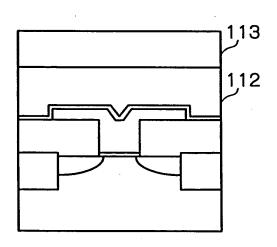
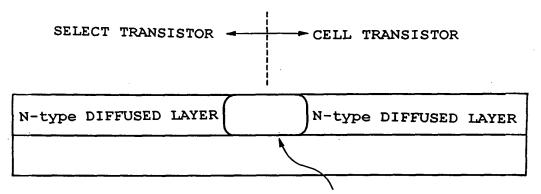


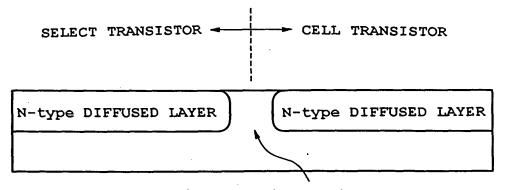
FIG. 10 (PRIOR ART)

APPROVED	O.G. FIG.		
BY	CLASS	SUBCLASS	
DRAFTSMAN			



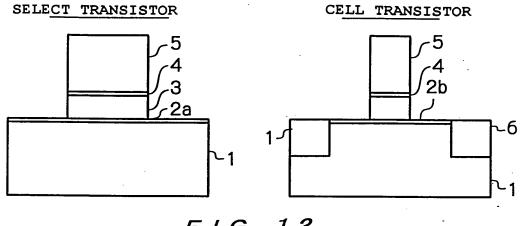
double implantation region of an impurity

FIG. 11A (PRIOR ART)

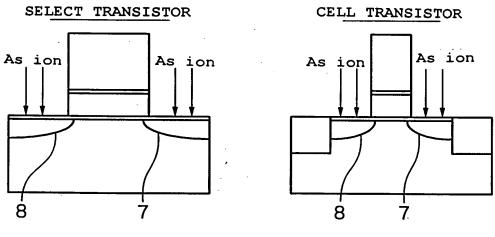


no implantation region of an impurity

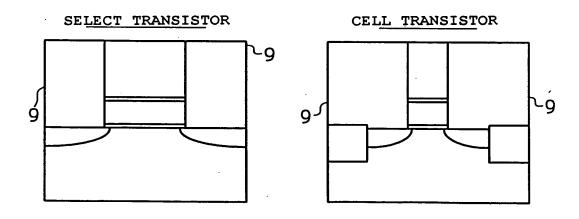
FIG. 11B (PRIOR ART)



F / G. 12



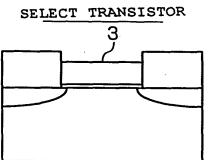
F I G. 13



F I G. 14

OGENETA LOINCO

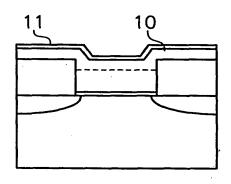




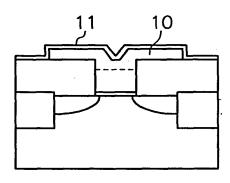
CELL TRANSISTOR

F | G. 15

SELECT TRANSISTOR

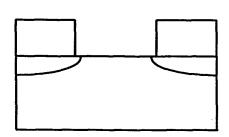


CELL TRANSISTOR

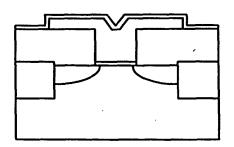


F I G. 16

SELECT TRANSISTOR



CELL TRANSISTOR

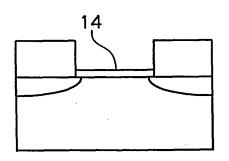


F I G. 17

DSSBBEGS IOISOO

CELL TRANSISTOR





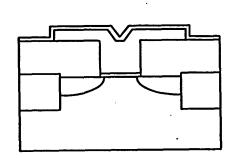
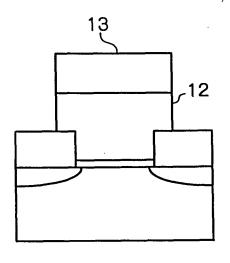


FIG. 18

SELECT TRANSISTOR



CELL TRANSISTOR

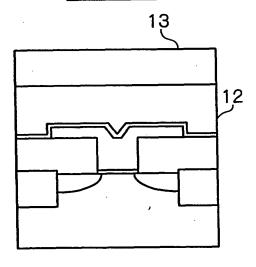
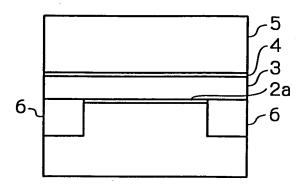
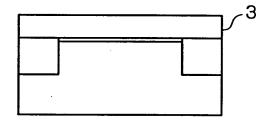


FIG. 19

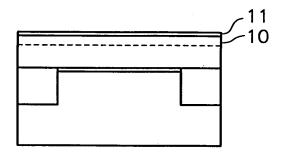
APPROVED	O.G. FIG.		
BY	CLASS	SUBCLASS	
DRAFTSMAN		1	



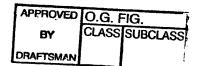
F I G. 20

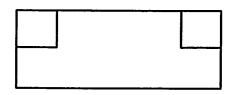


F I G. 21



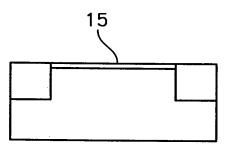
F I G. 22



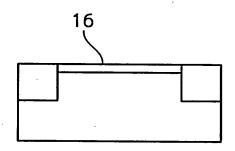


F I G. 23

Vcc-driven TRANSISTOR

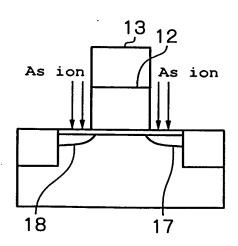


High-withstand-voltage TRANSISTOR

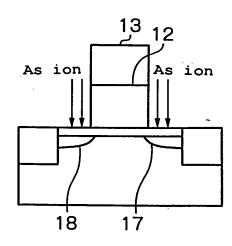


F I G. 24

Vcc-driven TRANSISTOR



High-withstand-voltage TRANSISTOR



F | G. 25